

**PATENT NUMBER and
ISSUE DATE**

U.S. UTILITY Patent Application

APPL NUM 10045523	FILING DATE 10/19/2001	CLASS 712- 716	SUBCLASS 13	GAU 2133	EXAMINER TJat
**APPLICANTS: Pannala Geetha; Geisler Steve; <i>SG</i>					
**CONTINUING DATA VERIFIED: <i>SG</i>					
** FOREIGN APPLICATIONS VERIFIED: <i>SG</i>					
PG-PUB <input type="checkbox"/>		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed		<input type="checkbox"/> yes <input checked="" type="checkbox"/> no		ATTORNEY DOCKET NO	
35 USC 119 conditions met		<input type="checkbox"/> yes <input checked="" type="checkbox"/> no		1011-59279	
Verified and Acknowledged Examiners's initials		<i>SG</i>			
TITLE : Method and apparatus to generate a wiring harness layout					

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NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drawg.	Figs Drawg.
		Print Fig.	
<input type="checkbox"/> TERMINAL DISCLAIMER		Application Examiner	
PREPARED FOR ISSUE			
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